



Lecture Objectives:

- Define the terms load balancing, parallelization, embarrassingly parallel
- Define the concepts of speedup and efficiency
- Explain Amdahl's law
- Calculate the maximum speedup that can be obtained when parallelizing an algorithm
- 5) Explain Foster's methodology and apply foster's methodology to a simple problem

```
double x[100], y[100]
                               11185015
for (int I = 0; I < 300; i++)/400(int 1 = 0; ....
                            x Li]+=yCiJ,
  X[i] += y[i];
```

Modify this so that it is easily parallelized...





Parallelization

- The process of converting a serial algorithm into a parallel algorithm
- Goals of parallelization
 - Roughly the same amount of work done by each processor
 - Aka load balancing
- Minimize communication between processors
 "Embarrassingly Parallel"
 - Programs that can be parallelized by simply dividing the work between two processors



Linear speedup

$$T_{Parallel} = \frac{T_{serial}}{p}$$

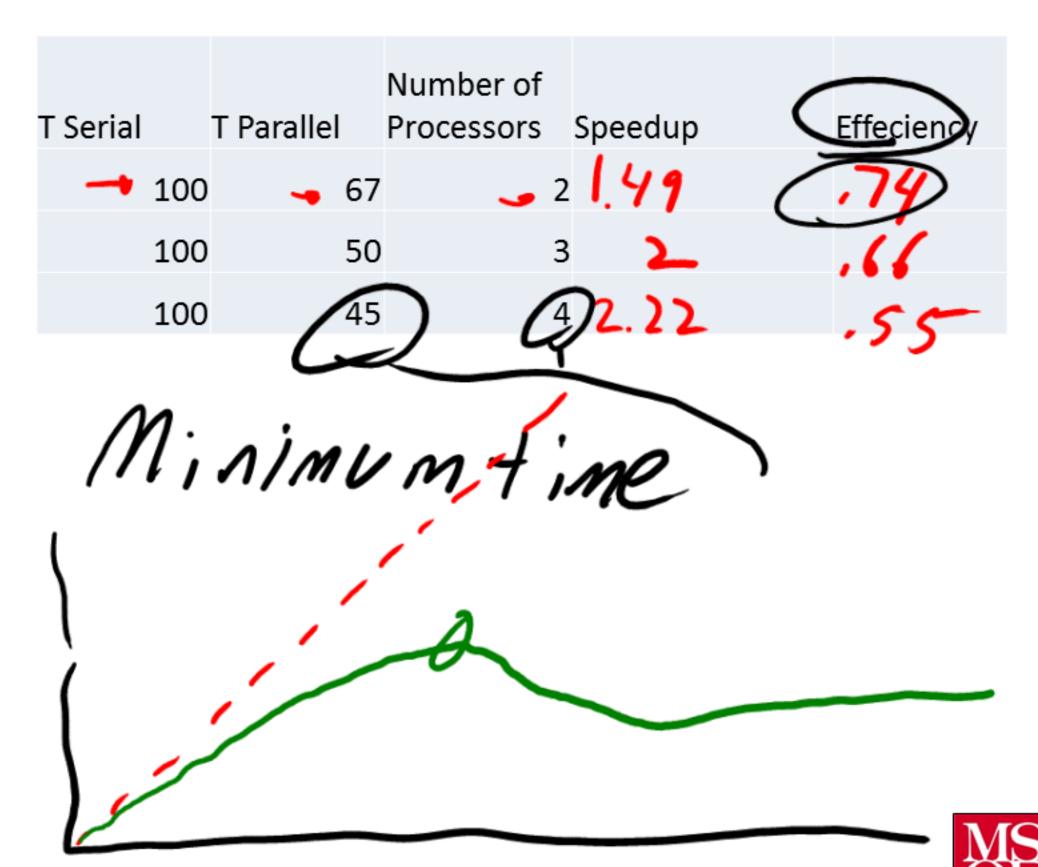
Speedup Definition

$$S = \frac{T_{serial}}{T_{parallel}}$$

Efficiency

$$E = \frac{S}{p} = \frac{T_{serial}}{T_{parallel}} = \frac{T_{serial}}{p \cdot T_{parallel}}$$





Lets work some examples

T Serial		Number of Processors	Sneedun	Effeciency
i Seriai	i raialici	1100633013	Speedup	Liteciency
100	67	2	1.492537	0.746269
100	50	3	2	0.666667
100	45	4	2.22222	0.555556



The ultimate equation

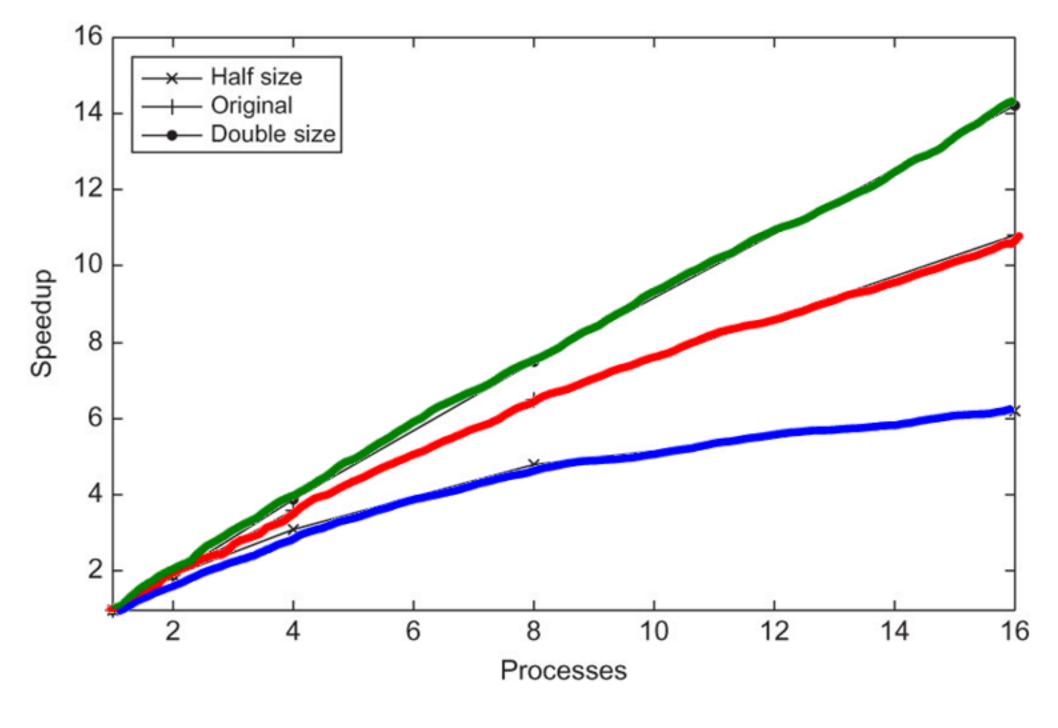
$$T_{parallel} = \frac{T_{serial}}{p} \left\{ T_{overhead} \right\}$$

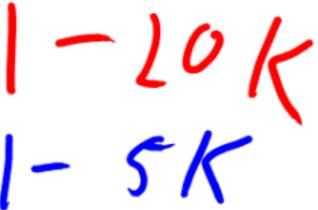
$$E_{verhead}$$

$$of family that the training of the property of the proper$$



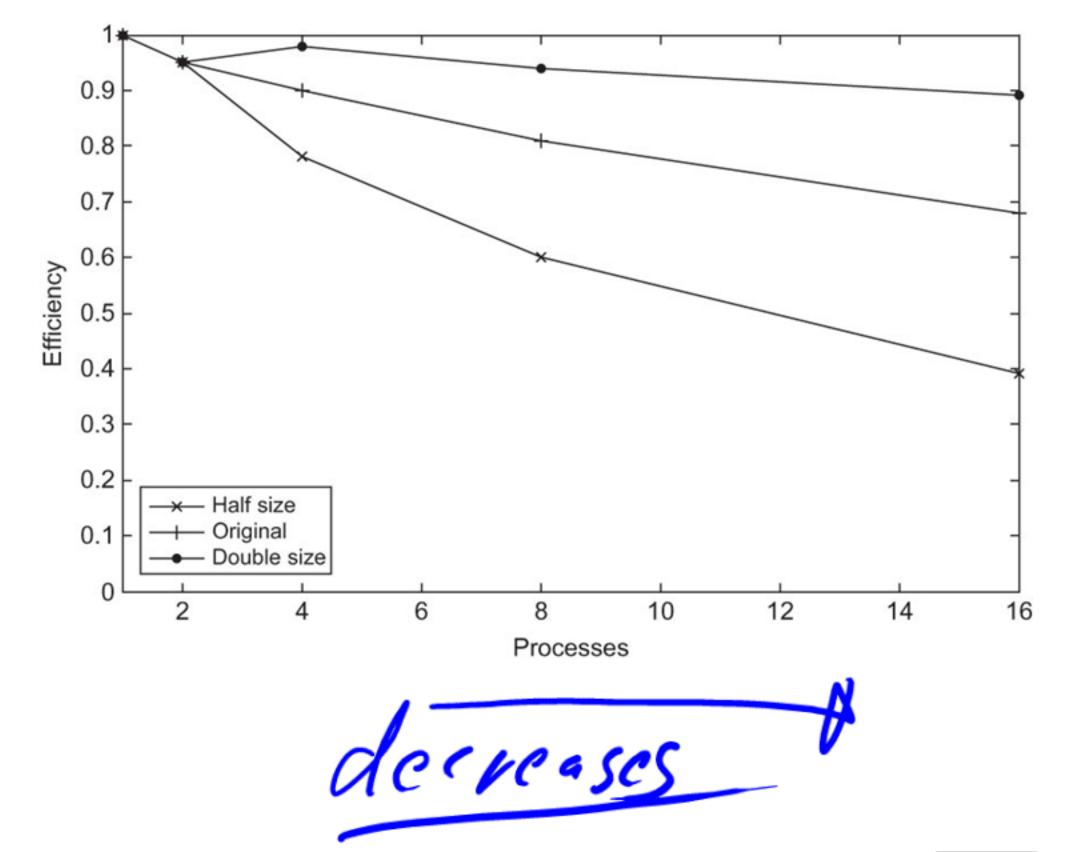












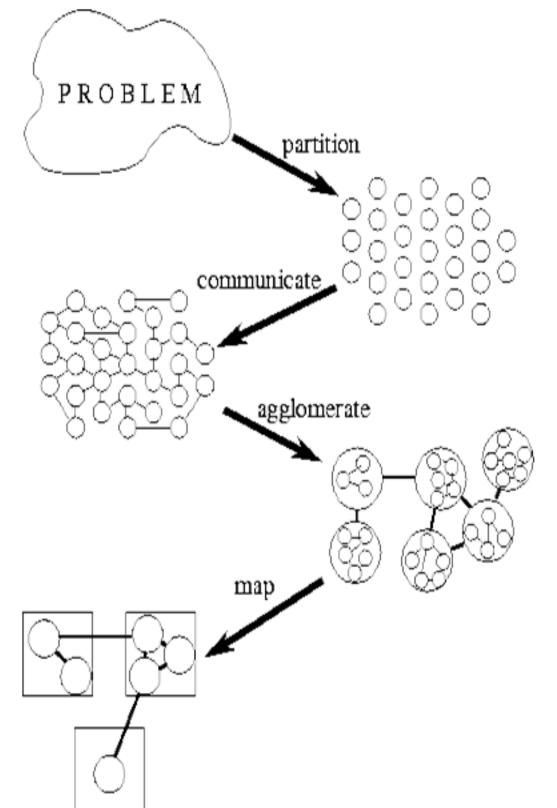


- The speedup of a program will be limited by how much of the program can be parallelized.
 - Example: A program takes 100 seconds to run and 80% of the code can be parallelized. What is the minimum time it will take to execute the program?

Minimum would be 203



Foster's Methodology



Partitioning

 The computation that is to be performed and the data operated on by this computation are decomposed into small tasks. Practical issues such as the number of processors in the target computer are ignored, and attention is focused on recognizing opportunities for parallel execution.

Communication

 The communication required to coordinate task execution is determined, and appropriate communication structures and algorithms are defined.

Agglomeration

 The task and communication structures defined in the first two stages of a design are evaluated with respect to performance requirements and implementation costs. If necessary, tasks are combined into larger tasks to improve performance or to reduce development costs.

Mapping

 Each task is assigned to a processor in a manner that attempts to satisfy the competing goals of maximizing processor utilization and minimizing communication costs. Mapping can be specified statically or determined at runtime by load-balancing algorithms.



- Lets assume we want to write a program which will layout music for printing
 - A song is composed of measures, notes, and rests.
 - We want to figure out how to best layout the music pages and render them to a pdf file for printing

