



Objectives

- Explain how a CPU Context switch occurs
- Explain how the hardware may impact the time necessary for a context switch (i.e. Sun Ultra Sparc)
- List reasons why a context switch would occur
- Explain why context switching can be bad
- Compare and contrast IO Bound and CPU Bound processes

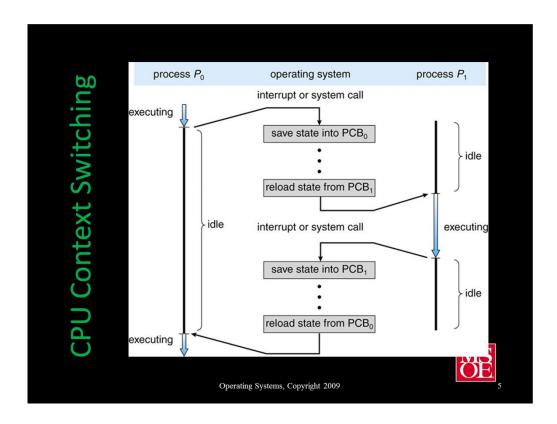


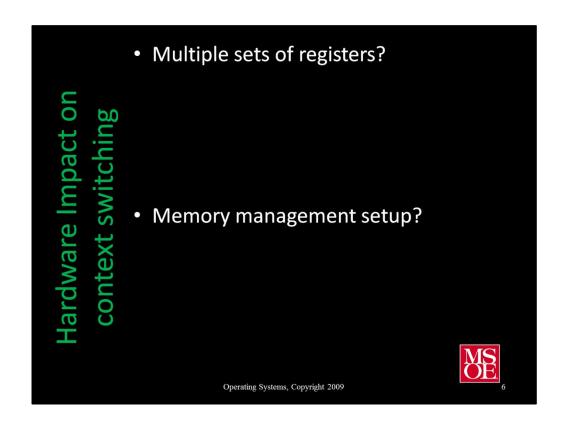
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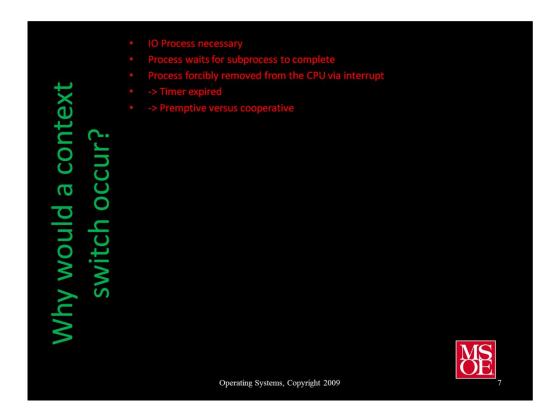
Store current state to a PCB Store contents of Micro general purpose registers Store the Stack pointer Store the program counter Load different PCB to the micro Program counter General Purpose registers Status register

Note that this must occur in the equivalent of an interrupt.

More complex on pipelined processors







IO Process necessary

Process waits for subprocess to complete

Process forcibly removed from the CPU via interrupt

- -> Timer expired
- -> Premptive versus cooperative

Why can context switching

- Assume the following:
 - A context switch takes 10 ms.
- a process spends 10ms running and is swapped out
- Second process runs for 5ms before being swapped out
- Two processes repeat back and forth 10 times.
- What percentage of time was spent doing real work?

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	Time (ms)	Context Switch	Process A	Process B	
<u>C-</u>		0	X		
30		5	X		
q	1	0 X			
O	1	5 X			
q	2	0		X	
60	2	5		X	
. ⊆	3	0 x			
4	3	5 x			
<u>it</u>	4	0	X		
>	4	5	X		
S	5	0		X	
×	5	5		X	
Ę	6	0 x			
	6	5 x			
8	7	0	X		
٥ ر	7	5	X		
ar	8	0 x			
Why can context switching be bad?	8	5 x			
<u> </u>	9	0		X	VIC.
X	9	5		X	MS
	10	0 x			
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CPU versus IO Bound

• IO Bound Process

- A process which spends most of its time doing input and output instead of CPU computations
- Too many IO processed may result in CPU doing nothing

• CPU Bound Process

- A process which spends most of its time doing computations
- Too many CPU bound processes may result in device queues going empty and ready queue overflowing



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